



EXPEDITED PROCEDURE – EXAMINING GROUP 2826

S/N 09/691004

PATENT

#20
Response
8/14/03
only

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Johannes P Mondt

Serial No.: 09/691004

Group Art Unit: 2826

Filed: October 18, 2000

Docket No.: 303.324US4

Title: TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND METHODS
OF FABRICATION AND USE

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

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Commissioner for Patents
P.O.Box 1450
Alexandria, VA 22313-1450

In response to the final Office Action dated 28 March 2003, the applicant respectfully requests reconsideration of the above-identified application in view of the following remarks.

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